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**Large area electronics with solution processable chemically derived graphene**

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The integration of novel materials such as single walled carbon nanotubes and nanowires into devices has been challenging. Similarly, although fundamental research on graphene has been prolific since its discovery, reports on making it technologically feasible for integration into devices have only recently appeared. In this presentation, a solution based method that allows uniform and controllable deposition of reduced graphene oxide thin films with thicknesses ranging from a single monolayer up to several layers over large areas will be described. The atomic and electronic structure along with the opto-electronics properties of graphene oxide at various degrees of reduction will be described.